

Docket No.: M4065.0322/P322-A

(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Eric T. Stubbs et al.

Application No.: 10/620,612

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Filed: July 17, 2003

For: INTEGRATED SEMICONDUCTOR

MEMORY CHIP WITH PRESENCE

DETECT DATA CAPABILITY

Confirmation No.: 9666

Art Unit: 2186

Examiner: M. D. Anderson

## RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed November 23, 2004 (Paper No. 20041118), Applicants hereby elects Group I, Claims 34-44 and 53-57 for continued examination. The election is made without traverse.

Allowance of the application is respectfully solicited.

Dated: December 2, 2004

Respectfully submitted,

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